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... thread-safe for deployment in **multi-threaded** server applications. ... on the number of CPUs (**processors**), regardless of ... as well as a dual-**processor** computer require ...
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Embedded Development: **Handling Exceptions** and Interrupts in eCos

... into the list using the **process** each time. ... The kernel API functions for Symmetric **Multi-Processing** (SMP) systems ... Specifies the **processor** to **handle** the hardware ...
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... of **processors** in a **multiprocessor** system increases ... to maintain cache coherence in **multi-processor** systems: ... shared, uncached, exclusive) b) **Processors** that have ...
courses.ece.uiuc.edu/ece511/Fall2002/finalques.pdf - [Similar pages](#)

Parallel Processing

... Fig.2. Distributed Memory **Multiprocessor** (DSM or NUMA ... Each **processor** must keep track of which cache blocks are ... MMX (**Multi-Media** Extention first appeared in the ...
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[PDF] Hardware and Software Support for Efficient **Exception Handling**

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... of its **multi-threaded** execution, the Tera **processor** ... table is inconsequential on modern **processors**, many of ... thus needed to indicate that the **processor** is already ...
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[PDF] RtWinAPI

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... s Win32 is a modern, **multi-process** and thread ... time-stamp clock, typically the **processor** clock, which ... advantage is less than 1 microsecond on current **processors**. ...
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[PDF] A novel method for adding **multiprocessor** support to a large and ...

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... discussion below, al- though **multi-threaded** processes ... performance issues relevant in **multiprocessor** systems [8 ... with data alignment, cross-**processor** cache access ...
www.ipd.bth.se/ska/ipdps2004.pdf - [Similar pages](#)

eCos kernel overview

... On a uni-**processor**, Scheduler::lock() is a simple ... implementation will only work in the **multi-level** queue ... Most **processors** have a simple interrupt mask bit in a ...
ecos.sourceware.org/docs-1.3.1/ref/ecos-ref.4.html - 17k - [Cached](#) - [Similar pages](#)

[Friday] Software as saviour for the modern **processor**?

... concept is similar to that of **multi-processing** tasks, where ... the overall cost of the **processor** will be ... reduced, fancy hardware in today's **processors** do branch ...
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[PDF] Improving **Exception Handling** in Multi-Agent Systems

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... own threads and use this **processing** power to ... an **Exception Handling** System Dedicated to **Multi-Agent** Systems ... **process** iterates (the whole **process** is illustrated ...
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Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	360	(common\$2 or shar\$4) near5 (interrupt\$4 or exception\$4) near5 handl\$4	US-PGPUB ; USPAT; EPO; JPO	OR	ON	2005/02/13 12:08
L2	108659	(second\$4 or different\$2) near10 (os or (operating adj1 system\$1))	US-PGPUB ; USPAT; EPO; JPO	OR	ON	2005/02/13 12:09
L3	145	I1 and I2	US-PGPUB ; USPAT; EPO; JPO	OR	ON	2005/02/13 12:10
L4	37	I3 and multi adj processor\$5	US-PGPUB ; USPAT; EPO; JPO	OR	ON	2005/02/13 12:13
L5	30	I4 and (processor near5 number)	US-PGPUB ; USPAT; EPO; JPO	OR	ON	2005/02/13 12:13
L6	30	(US-20040237086-\$ or US-20030037178-\$ or US-20020184292-\$ or US-20020065989-\$ or US-20020062434-\$ or US-20010016879-\$).did. or (US-6789181-\$ or US-6789156-\$ or US-6779107-\$ or US-6772419-\$ or US-6742104-\$ or US-6742103-\$ or US-6738847-\$ or US-6711605-\$ or US-6687818-\$ or US-6665761-\$ or US-6549959-\$ or US-6397379-\$ or US-6314501-\$ or US-5701502-\$ or US-5471609-\$ or US-5388215-\$ or US-5369767-\$ or US-5369749-\$ or US-5363497-\$ or US-5325517-\$ or US-5283868-\$ or US-5155809-\$ or US-5144692-\$ or US-5113522-\$).did.	US-PGPUB ; USPAT	OR	OFF	2005/02/13 12:13
L7	30	I6 and (processor near5 number)	US-PGPUB ; USPAT; EPO; JPO	OR	ON	2005/02/13 12:16

L8	2273	processor near5 register near5 number	US-PGPUB ; USPAT; EPO; JPO	OR	ON	2005/02/13 12:16
L9	60	processor near5 register near5 number near5 identif\$5	US-PGPUB ; USPAT; EPO; JPO	OR	ON	2005/02/13 12:16
L10	7	l9 and (multi adj processor\$5)	US-PGPUB ; USPAT; EPO; JPO	OR	ON	2005/02/13 12:24
L11	3	exception near5 processor near5 number near5 register and (multi adj processor\$5)	US-PGPUB ; USPAT; EPO; JPO	OR	ON	2005/02/13 12:26
L12	1	identif\$3 near5 exception near5 processor near5 register and (multi adj processor\$5)	US-PGPUB ; USPAT; EPO; JPO	OR	ON	2005/02/13 12:26
L13	2	identif\$3 near5 processor near5 number same exception and (multi adj processor\$5)	US-PGPUB ; USPAT; EPO; JPO	OR	ON	2005/02/13 12:30
L14	43	identif\$3 near5 processor near5 number and exception and (multi adj processor\$5)	US-PGPUB ; USPAT; EPO; JPO	OR	ON	2005/02/13 12:30

L15	43	(US-20050027972-\$ or US-20040133516-\$ or US-20040034678-\$ or US-20020065989-\$ or US-20020062434-\$).did. or (US-6742104-\$ or US-6742103-\$ or US-6651163-\$ or US-6609189-\$ or US-6442585-\$ or US-6332178-\$ or US-6237059-\$ or US-6202127-\$ or US-6112016-\$ or US-6026504-\$ or US-5951662-\$ or US-5918248-\$ or US-5822767-\$ or US-5802338-\$ or US-5651137-\$ or US-5634034-\$ or US-5629950-\$ or US-5564008-\$ or US-5557759-\$ or US-5555382-\$ or US-5553266-\$ or US-5553258-\$ or US-5517628-\$ or US-5485594-\$ or US-5428766-\$ or US-5410654-\$).did. or (US-5388224-\$ or US-5361267-\$ or US-5319766-\$ or US-5233702-\$ or US-5047919-\$ or US-5021945-\$ or US-4893234-\$ or US-4669043-\$ or US-4253146-\$ or US-4253144-\$ or US-4245306-\$ or US-4240143-\$).did.	US-PGPUB ; USPAT	OR	OFF	2005/02/13 12:30
L16	43	l15 and identif\$3 near5 processor near5 number	US-PGPUB ; USPAT; EPO; JPO	OR	ON	2005/02/13 12:37
L17	0	identifier near5 processor and (muti adj processor)	US-PGPUB ; USPAT; EPO; JPO	OR	ON	2005/02/13 12:38
L18	4227	identifier near5 processor	US-PGPUB ; USPAT; EPO; JPO	OR	ON	2005/02/13 12:38

L19	545	processor adj identifier	US-PGPUB ; USPAT; EPO; JPO	OR	ON	2005/02/13 12:38
L20	38	processor adj identifier near5 register	US-PGPUB ; USPAT; EPO; JPO	OR	ON	2005/02/13 13:07
L21	2	processor near register near5 PID	US-PGPUB ; USPAT; EPO; JPO	OR	ON	2005/02/13 13:13
L22	6	processor near register same PID	US-PGPUB ; USPAT; EPO; JPO	OR	ON	2005/02/13 13:15
L23	4	l22 not l21	US-PGPUB ; USPAT; EPO; JPO	OR	ON	2005/02/13 13:13
L24	402	processor near register near5 number	US-PGPUB ; USPAT; EPO; JPO	OR	ON	2005/02/13 13:15
L25	199	processor near register near2 number	US-PGPUB ; USPAT; EPO; JPO	OR	ON	2005/02/13 13:15
L26	118	processor near register near number	US-PGPUB ; USPAT; EPO; JPO	OR	ON	2005/02/13 13:16
L27	21	l26 and (multi adj processor\$5)	US-PGPUB ; USPAT; EPO; JPO	OR	ON	2005/02/13 13:15
L28	21	(US-20020059509-\$).did. or (US-6532530-\$ or US-6026504-\$ or US-5991874-\$ or US-5893156-\$ or US-5857103-\$ or US-5809320-\$ or US-5481689-\$ or US-5446841-\$ or US-5430885-\$ or US-5394529-\$ or US-5377336-\$ or US-5333296-\$ or US-5317720-\$ or US-5193187-\$ or US-5155843-\$ or US-5033047-\$ or US-5010477-\$ or US-4442321-\$ or US-4259549-\$ or US-4256926-\$).did.	US-PGPUB ; USPAT	OR	OFF	2005/02/13 13:16
L29	21	l28 and processor near register near number	US-PGPUB ; USPAT; EPO; JPO	OR	ON	2005/02/13 13:18

L30	0	l28 and processor near register near unique	US-PGPUB ; USPAT; EPO; JPO	OR	ON	2005/02/13 13:18
L31	1	processor near register near unique	US-PGPUB ; USPAT; EPO; JPO	OR	ON	2005/02/13 13:19
L32	1	processor near register near5 unique near identification	US-PGPUB ; USPAT; EPO; JPO	OR	ON	2005/02/13 13:20
L33	206	processor near5 identifier near5 register	US-PGPUB ; USPAT; EPO; JPO	OR	ON	2005/02/13 13:26
L34	50	processor near2 identifier near2 register	US-PGPUB ; USPAT; EPO; JPO	OR	ON	2005/02/13 13:21
L35	8	l34 and multi adj processor\$5	US-PGPUB ; USPAT; EPO; JPO	OR	ON	2005/02/13 13:21
L36	4	processor near5 identifier same processor near within near2 register	US-PGPUB ; USPAT; EPO; JPO	OR	ON	2005/02/13 14:15
L37	0	processor near5 identifier near5 regist	US-PGPUB ; USPAT; EPO; JPO	OR	ON	2005/02/13 14:16
L38	206	processor near5 identifier near5 register	US-PGPUB ; USPAT; EPO; JPO	OR	ON	2005/02/13 14:16
L39	13	processor near identifier near register	US-PGPUB ; USPAT; EPO; JPO	OR	ON	2005/02/13 14:19
L40	8	processor near identifier near bit	US-PGPUB ; USPAT; EPO; JPO	OR	ON	2005/02/13 14:35
L41	9	processor near own near identify	US-PGPUB ; USPAT; EPO; JPO	OR	ON	2005/02/13 14:37
L42	8	processor near itself near identify	US-PGPUB ; USPAT; EPO; JPO	OR	ON	2005/02/13 14:49
L43	172079	processor near2 itself near2 identify same exception or interrupt	US-PGPUB ; USPAT; EPO; JPO	OR	ON	2005/02/13 14:50
L44	0	processor near2 itself near2 identify same (exception or interrupt)	US-PGPUB ; USPAT; EPO; JPO	OR	ON	2005/02/13 14:52
L45	28	processor near2 itself near2 identify	US-PGPUB ; USPAT; EPO; JPO	OR	ON	2005/02/13 14:58

L46	1	("6,651,163").PN.	USPAT	OR	OFF	2005/02/13 15:05
L47	2	((("6,425,039") or ("2002007131") or ("6,006, 247"))).PN.	USPAT	OR	OFF	2005/02/13 15:06
L48	2	((("6,425,039") or ("2002007131") or ("6,006, 247"))).PN.	US-PGPUB ; USPAT	OR	OFF	2005/02/13 15:08
L49	428	Brenner and exception	US-PGPUB	OR	OFF	2005/02/13 15:18
L50	1	("6615303").PN.	USPAT	OR	OFF	2005/02/13 15:18
S1	59	(multi adj process\$3) same exception	USPAT	OR	ON	2005/02/04 11:34
S2	165	((device adj driver) same serial) same memory	USPAT	OR	ON	2003/11/13 17:59
S3	43	(multi adj process\$3) same exception same memory	USPAT	OR	ON	2003/11/13 17:59
S4	14	((multi adj process\$3) same exception same memory) and (process\$3 same (id or identification))	USPAT	OR	ON	2003/11/13 18:06
S5	3	((multi adj process\$3) same exception same memory) and (process\$3 same (id or identification))) and (interrupt near2 handl\$3)	USPAT	OR	ON	2003/11/13 18:00
S6	34	((multi adj process\$3) same exception same memory) and (process\$3 same (identif\$8))	USPAT	OR	ON	2003/11/13 18:02
S7	17	((multi adj process\$3) same exception same memory) and (process\$3 near3 (identif\$8))	USPAT	OR	ON	2003/11/13 18:04
S8	0	((multi adj process\$3) same exception same memory) and (process\$3 near3 (identif\$8))) and (vector near5 space)	USPAT	OR	ON	2003/11/13 18:04
S9	8	((multi adj process\$3) same exception same memory) and (process\$3 near3 (identif\$8))) and (address near5 space)	USPAT	OR	ON	2003/11/13 18:04
S10	5	((multi adj process\$3) same exception same memory) and (processor near3 (id or identification))	USPAT	OR	ON	2003/11/13 18:07
S11	3667	(processor near3 (id or identification))	USPAT	OR	ON	2003/11/13 18:08

S12	823	((processor near3 (id or identification))) and exception	USPAT	OR	ON	2003/11/13 18:08
S13	191	(processor near3 (id or identification)).ab.	USPAT	OR	ON	2003/11/13 18:08
S14	21	(processor near3 (id or identification)) and exception.ab.	USPAT	OR	ON	2003/11/13 18:18
S15	80	(processor near3 (id or identif\$)) and exception.ab.	USPAT	OR	ON	2003/11/13 18:19
S16	34	(processor near3 (id or identif\$)) and exception and (multi adj process\$).ab.	USPAT	OR	ON	2003/11/13 18:19
S17	0	sanjay-lal.in.	USPAT	OR	ON	2003/11/13 18:24
S18	4663	redback networks.as.	USPAT	OR	ON	2003/11/13 18:24
S19	9	redback adj networks.as.	USPAT	OR	ON	2003/11/13 18:25
S20	0	(redback adj networks.as.) and exception	USPAT	OR	ON	2003/11/13 18:25
S21	6	(different near (operating adj system)) same (multi adj processor)	USPAT	OR	ON	2003/11/13 18:27
S22	2	((different near (operating adj system)) same (multi adj processor)) and exception	USPAT	OR	ON	2003/11/13 18:26
S23	115	(different near (operating adj system)) and (multi adj processor)	USPAT	OR	ON	2003/11/13 18:27
S24	22	((different near (operating adj system)) and (multi adj processor)) and (handl\$3 near3 exception)	USPAT	OR	ON	2003/11/13 18:27
S25	17	"5606696".URPN.	USPAT	OR	OFF	2003/11/13 18:31
S26	9	"5606696".URPN. and (multi adj processor)	USPAT	OR	OFF	2003/11/13 18:32
S27	4	("5606696".URPN. and (multi adj processor)) and exception	USPAT	OR	OFF	2003/11/13 18:32
S28	13	((multi adj processor) same interrupt same (processor near3 (id or identif\$8)))	USPAT	OR	ON	2003/11/13 18:37
S29	547	multi adj processor and (interrupt near2 handl\$3)	USPAT	OR	ON	2003/11/14 18:17
S30	481	(multi adj processor and (interrupt near2 handl\$3)) and register	USPAT	OR	ON	2003/11/14 17:52

S31	36	((multi adj processor and (interrupt near2 handl\$3)) and register) and (exception near5 processor)	USPAT	OR	ON	2003/11/14 18:18
S32	11	((multi adj processor and (interrupt near2 handl\$3)) and register) and (exception near5 processor)) and (common near5 interrupt)	USPAT	OR	ON	2003/11/14 18:18
S33	1	common near5 (exception or interrupt) near5 (address adj space)	USPAT	OR	ON	2003/11/14 18:01
S34	323	(exception or interrupt) near5 (address adj space)	USPAT	OR	ON	2003/11/14 17:57
S35	21	((exception or interrupt) near5 (address adj space)) and (multi adj processor and (interrupt near2 handl\$3))	USPAT	OR	ON	2003/11/14 18:01
S36	21	((exception or interrupt) near5 (address adj space)) and (multi adj processor and (interrupt near2 handl\$3))	US-PGPUB ; USPAT; EPO; JPO	OR	ON	2003/11/14 18:01
S37	3	common near5 (exception or interrupt) near5 (address adj space)	US-PGPUB ; USPAT; EPO; JPO	OR	ON	2003/11/14 18:03
S38	0	714/10-13.ccls.	US-PGPUB ; USPAT; EPO; JPO	OR	ON	2003/11/14 18:03
S39	1169	714/10-13.ccls.	US-PGPUB ; USPAT; EPO; JPO	OR	ON	2003/11/14 18:04
S40	2337	712/10-31.ccls.	US-PGPUB ; USPAT; EPO; JPO	OR	ON	2003/11/14 18:05
S41	384	712/244.ccls.	US-PGPUB ; USPAT; EPO; JPO	OR	ON	2003/11/14 18:05
S42	0	714/10-13.ccls. and 712/10-31.ccls. and 712/244.ccls.	US-PGPUB ; USPAT; EPO; JPO	OR	ON	2003/11/14 18:06
S43	3820	714/10-13.ccls. or 712/10-31.ccls. or 712/244.ccls.	US-PGPUB ; USPAT; EPO; JPO	OR	ON	2003/11/14 18:06
S44	82	(714/10-13.ccls. or 712/10-31.ccls. or 712/244.ccls.) and (multi adj processor and (interrupt near2 handl\$3))	US-PGPUB ; USPAT; EPO; JPO	OR	ON	2003/11/14 18:06

S45	423	(714/10-13.ccls. or 712/10-31. ccls. or 712/244.ccls.) and (multi adj processor)	US-PGPUB ; USPAT; EPO; JPO	OR	ON	2003/11/14 18:14
S46	22	((714/10-13.ccls. or 712/10-31. ccls. or 712/244.ccls.) and (multi adj processor)) and (exception or interrupt).ab.	US-PGPUB ; USPAT; EPO; JPO	OR	ON	2003/11/14 18:14
S47	2	"6272618".URPN.	USPAT	OR	OFF	2003/11/14 18:09
S48	1040	(714/10-13.ccls. or 712/10-31. ccls. or 712/244.ccls.) and ((multi adj processor) or multiprocessor)	US-PGPUB ; USPAT; EPO; JPO	OR	ON	2003/11/14 18:14
S49	9	((714/10-13.ccls. or 712/10-31. ccls. or 712/244.ccls.) and (multi adj processor)) and ((exception or interrupt) and (multiprocessor or (multi adj processor))).ab.	US-PGPUB ; USPAT; EPO; JPO	OR	ON	2003/11/14 18:15
S50	1124	(multi adj processor or multiprocessor) and (interrupt near2 handl\$3) and register	USPAT	OR	ON	2003/11/14 18:21
S51	106	((multi adj processor or multiprocessor) and (interrupt near2 handl\$3) and register) and (exception near5 processor)	USPAT	OR	ON	2003/11/14 18:18
S52	28	((multi adj processor or multiprocessor) and (interrupt near2 handl\$3) and register) and (exception near5 processor)) and (common near5 interrupt)	USPAT	OR	ON	2003/11/14 18:19
S53	2	((714/10-13.ccls. or 712/10-31. ccls. or 712/244.ccls.) and ((multi adj processor) or multiprocessor)) and (common near5 interrupt near handler)	USPAT	OR	ON	2003/11/14 18:19
S54	16	((multi adj processor or multiprocessor) and exception and memory).ab.	US-PGPUB ; USPAT; EPO; JPO	OR	ON	2003/11/14 18:28
S55	0	((multi adj processor or multiprocessor) and exception and memory and handl\$%).ab.	US-PGPUB ; USPAT; EPO; JPO	OR	ON	2003/11/14 18:28
S56	3	((multi adj processor or multiprocessor) and exception and memory and handl\$5).ab.	US-PGPUB ; USPAT; EPO; JPO	OR	ON	2003/11/14 18:32
S57	2969	((multi adj processor or multiprocessor) and exception and memory and handl\$5)	US-PGPUB ; USPAT; EPO; JPO	OR	ON	2003/11/14 18:30

S58	103	((multi adj processor or multiprocessor) same exception same memory)	US-PGPUB ; USPAT; EPO; JPO	OR	ON	2003/11/14 18:32
S59	27	((multi adj processor or multiprocessor) same exception same memory) and (handl\$5 near5 exception)	US-PGPUB ; USPAT; EPO; JPO	OR	ON	2003/11/14 18:31
S60	19	((multi adj processor or multiprocessor) and interrupt and memory and handl\$5).ab.	US-PGPUB ; USPAT; EPO; JPO	OR	ON	2003/11/14 18:39
S61	16	((multi adj processor or multiprocessor) and exception and memory).ab.	US-PGPUB ; USPAT; EPO; JPO	OR	ON	2003/11/14 18:45
S62	103	((multi adj processor or multiprocessor) same exception same memory)	US-PGPUB ; USPAT; EPO; JPO	OR	ON	2003/11/14 18:45
S63	16	((multi adj processor or multiprocessor) same exception same memory) and (memory same vector same address)	US-PGPUB ; USPAT; EPO; JPO	OR	ON	2003/11/14 18:49
S64	17	((multi adj processor or multiprocessor) same exception same memory) and (identif\$5 near2 processor)	US-PGPUB ; USPAT; EPO; JPO	OR	ON	2003/11/14 18:49
S65	3590	(multi adj processor or multiprocessor) and exception and memory	US-PGPUB ; USPAT; EPO; JPO	OR	ON	2003/11/20 20:14
S66	412	((multi adj processor or multiprocessor) and exception and memory) and (register same vector same memory)	US-PGPUB ; USPAT; EPO; JPO	OR	ON	2003/11/20 20:22
S67	2	((multi adj processor or multiprocessor) and exception and memory) and (register same vector same memory same (processor near5 (id or ident\$9)))	US-PGPUB ; USPAT; EPO; JPO	OR	ON	2003/11/20 20:19
S68	185	((multi adj processor or multiprocessor) and exception and memory) and (register same vector same memory) and (processor near5 (id or ident\$9))	US-PGPUB ; USPAT; EPO; JPO	OR	ON	2003/11/20 20:21
S69	99	((multi adj processor or multiprocessor) and exception and memory) and (register same vector same memory) and ((processor near5 (id or ident\$9)) same register)	US-PGPUB ; USPAT; EPO; JPO	OR	ON	2003/11/20 20:22

S73	22	((multi adj processor or multiprocessor) and exception and memory) and (register same vector same memory)) and (processor near5 handle\$5 near5 exception)	US-PGPUB ; USPAT; EPO; JPO	OR	ON	2003/11/20 20:27
S74	22	((US-5325517-\$ or US-5363497-\$ or US-5369749-\$ or US-5369767-\$ or US-5388215-\$ or US-5596713-\$ or US-5155809-\$ or US-5283868-\$ or US-5113522-\$ or US-5144692-\$ or US-5701502-\$ or US-5838984-\$ or US-5996058-\$ or US-6003129-\$ or US-6006247-\$ or US-6058465-\$ or US-6061711-\$ or US-6161162-\$ or US-6397379-\$ or US-6549959-\$).did. or (US-20020184292-\$ or US-20030126520-\$).did.) and (processor near5 handle\$5 near5 exception)	US-PGPUB ; USPAT; EPO; JPO	OR	ON	2003/11/20 20:39
S75	14	((dedicat\$5 near5 processor) same handl\$5 same exception)	US-PGPUB ; USPAT; EPO; JPO	OR	ON	2003/11/20 20:47
S76	11	((assigned near5 processor) same handl\$5 same exception)	US-PGPUB ; USPAT; EPO; JPO	OR	ON	2003/11/20 20:52
S77	11	((assigned adj2 processor) same exception)	US-PGPUB ; USPAT; EPO; JPO	OR	ON	2003/11/20 20:53
S78	19	((allocat\$3 adj2 processor) same exception)	US-PGPUB ; USPAT; EPO; JPO	OR	ON	2003/11/20 20:55
S79	18	((common near5 vector) same exception)	US-PGPUB ; USPAT; EPO; JPO	OR	ON	2003/11/20 20:56
S81	0	719-310-322.ccls.	US-PGPUB ; USPAT; EPO; JPO	OR	ON	2005/02/04 11:35
S82	3038	719/311-322.ccls.	US-PGPUB ; USPAT; EPO; JPO	OR	ON	2005/02/04 11:35

S83	722	719/310.ccls.	US-PGPUB ; USPAT; EPO; JPO	OR	ON	2005/02/04 11:35
S84	3667	S83 or S82	US-PGPUB ; USPAT; EPO; JPO	OR	ON	2005/02/04 11:35
S85	1675	709/201.ccls.	US-PGPUB ; USPAT; EPO; JPO	OR	ON	2005/02/04 11:35
S86	1381	714/10-13.ccls.	US-PGPUB ; USPAT; EPO; JPO	OR	ON	2005/02/04 11:36
S87	1647	712/344,28-31,10-22.ccls.	US-PGPUB ; USPAT; EPO; JPO	OR	ON	2005/02/04 11:36
S88	8118	S84 or S85 or S86 or S87	US-PGPUB ; USPAT; EPO; JPO	OR	ON	2005/02/04 11:36
S89	612	S88 and (exception or fault) same (common or shar\$3)	US-PGPUB ; USPAT; EPO; JPO	OR	ON	2005/02/04 11:38
S90	340	S89 and (multiprocessor\$3 or (multi adj processor) or ((plural\$4 or multiple\$5) near2 processor))	US-PGPUB ; USPAT; EPO; JPO	OR	ON	2005/02/04 11:40
S91	113	S90 and ((interrupt or exception) near2 handl\$3)	US-PGPUB ; USPAT; EPO; JPO	OR	ON	2005/02/04 11:40